

FIG.1

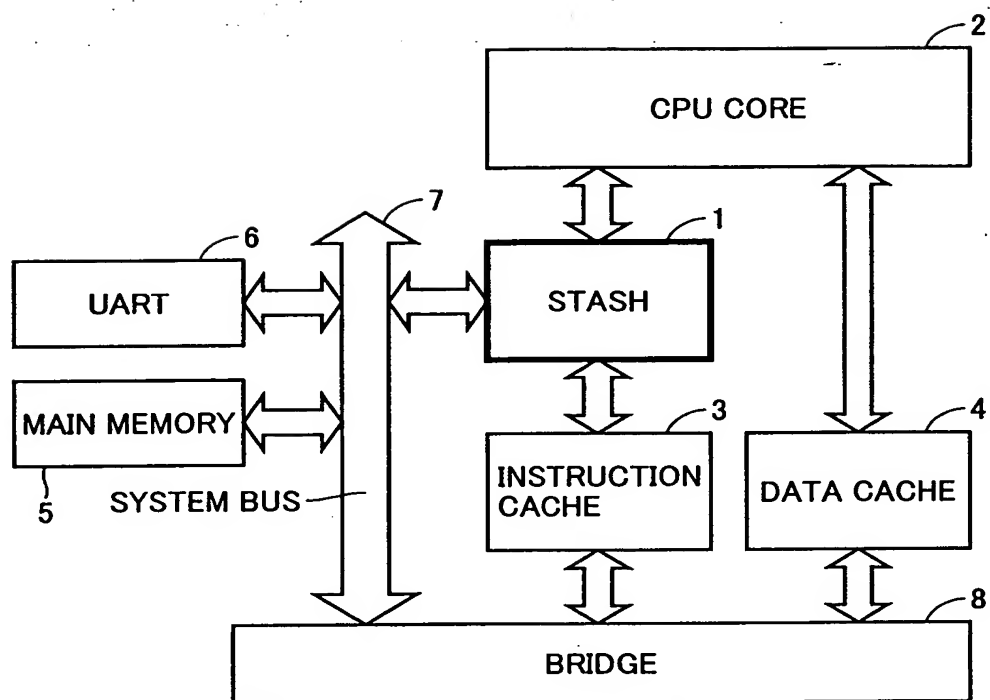


FIG.2

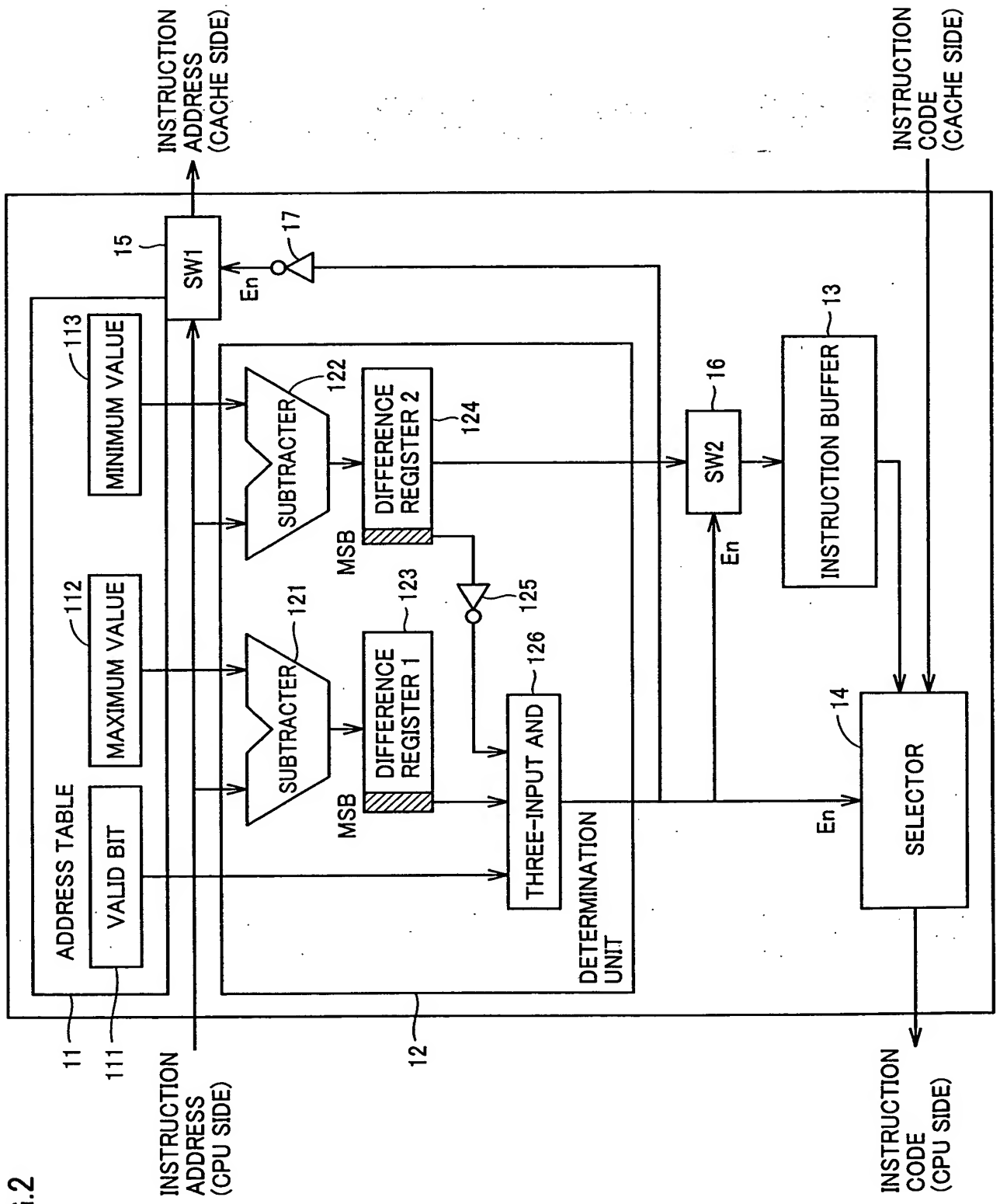


FIG.3

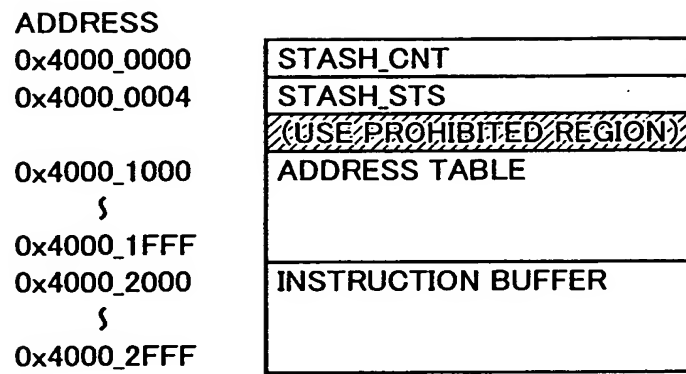


FIG.4

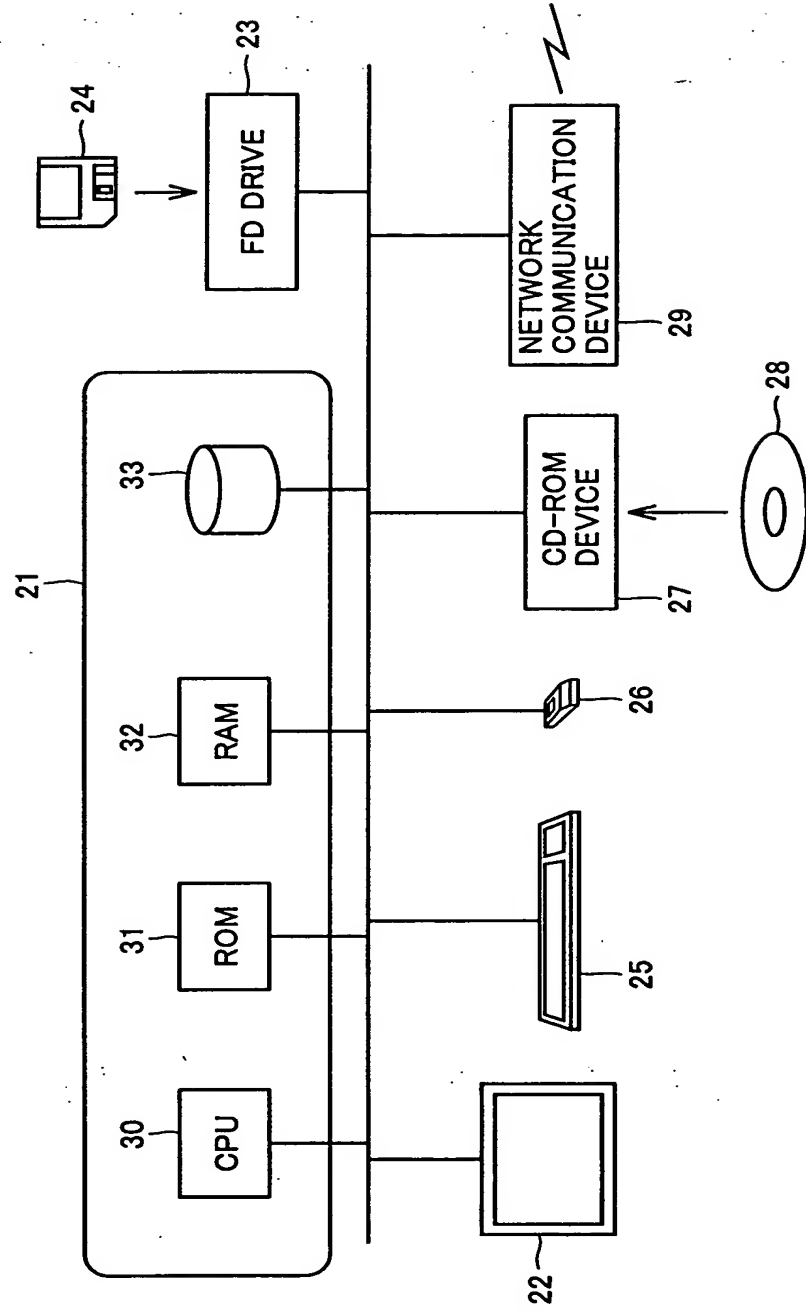


FIG.5

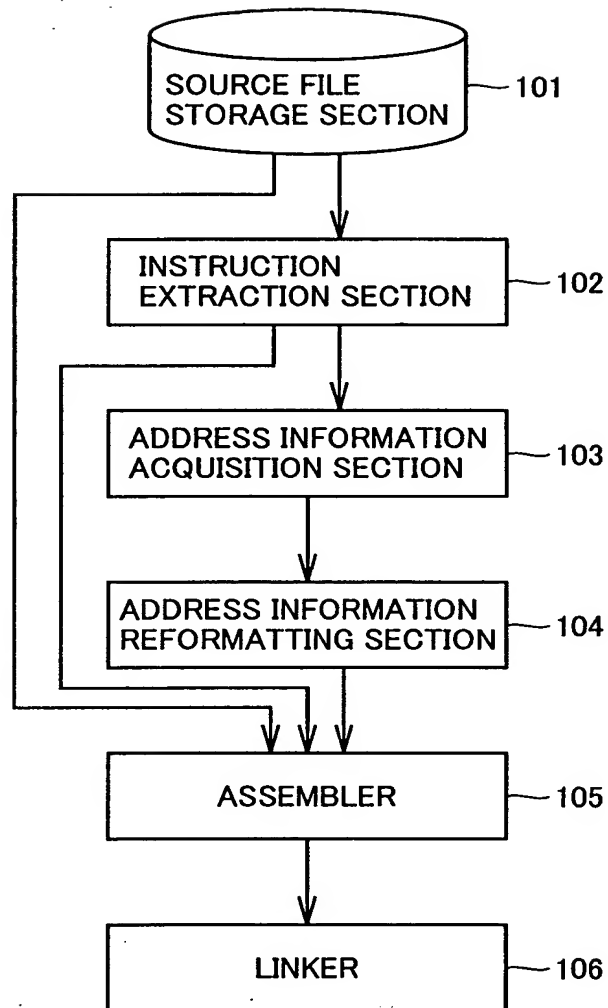


FIG.6

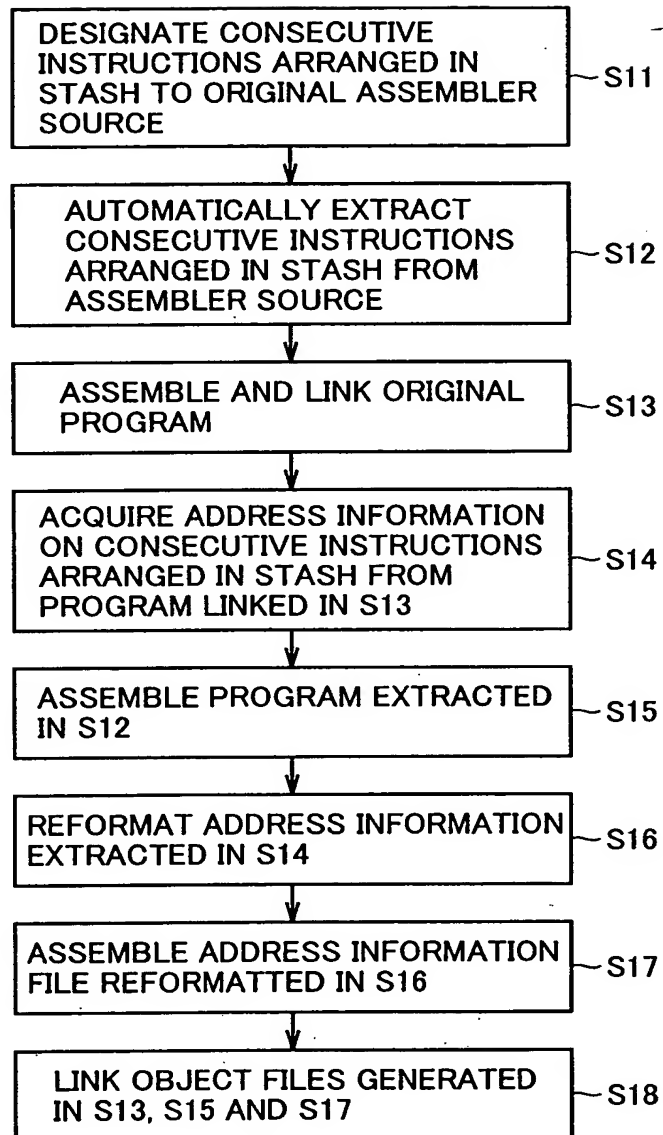


FIG.7A

```
.text
_func1:
_stash1_top:
ld r1, @r14+
ld r2, @r14+
_stash1_end:
add r1, r2
(THE REST IS OMITTED)
```

FIG.7B

```
ld r1, @r14+
ld r2, @r14+
```

FIG.7C

```
.section _stash_at, "a"
.word 0x00000264, 0x0000026c
```

FIG.7D

```
Disassembly of section .stash_at:

40001000 <.stash_at>:
      0: 00 00 02 64      subv r0,r0→ cmpeq r2,r4
      4: 00 00 02 6c      subv r0,r0→ cmpeq r2,r12
Disassembly of section .stash_buf:

40002000 <.stash_buf>:
      0: 21 ee 22 ee      ld r1,@lr→ ld r2,@lr+
```

FIG.8C

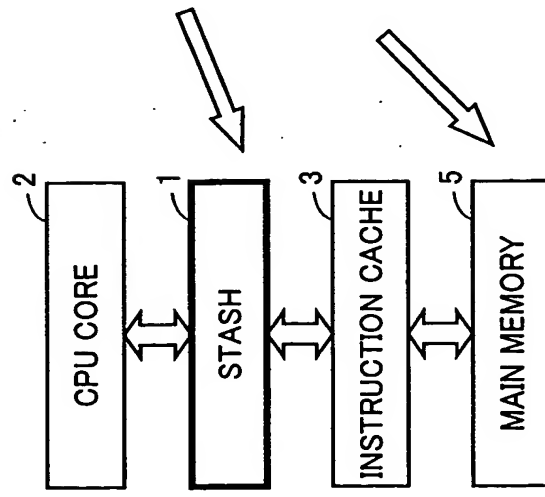


FIG.8B

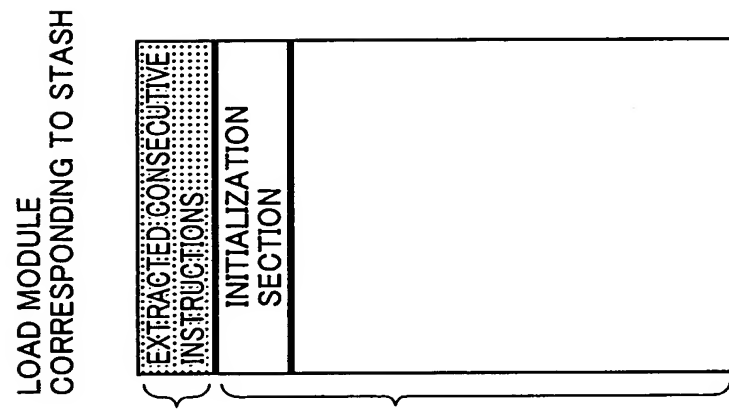


FIG.8A

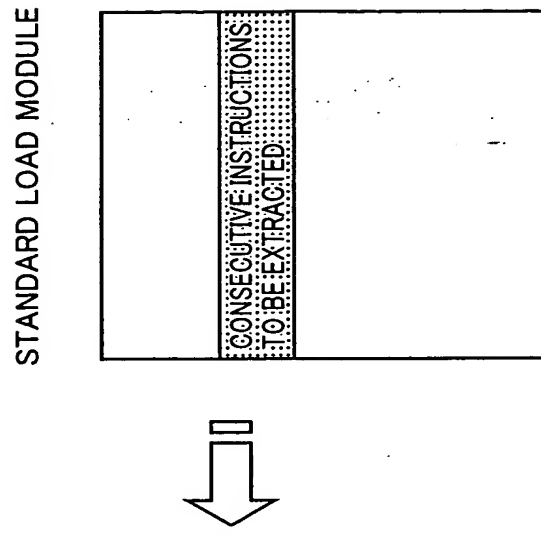




FIG.9

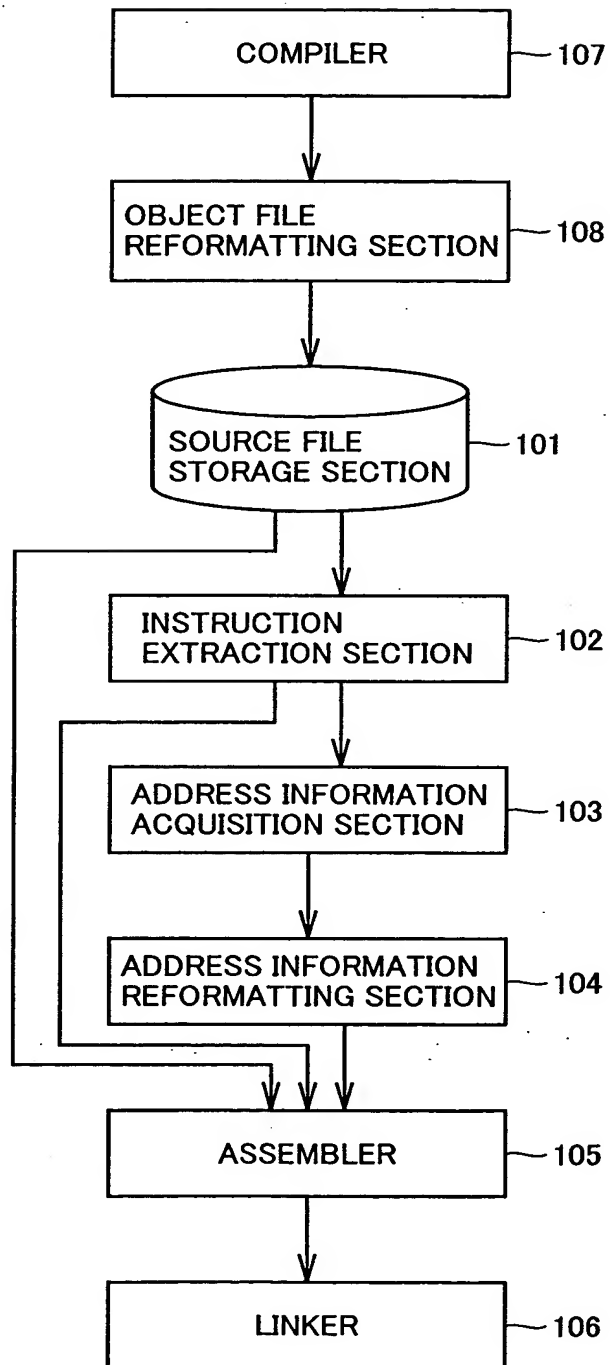


FIG.10

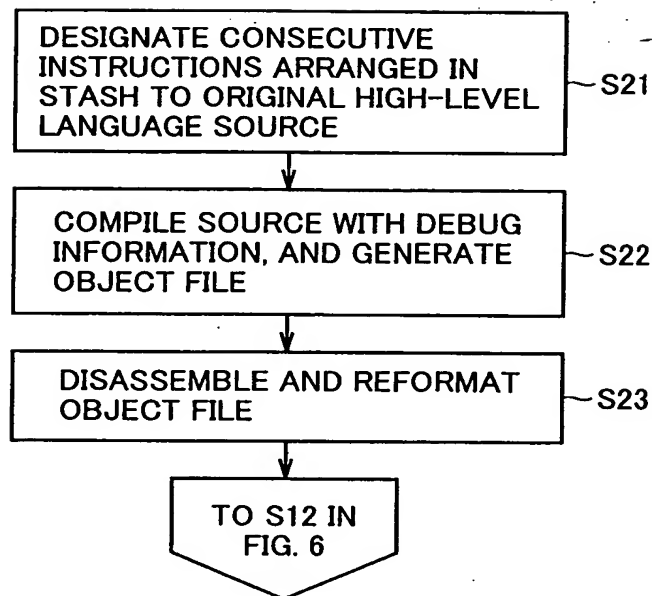


FIG.11

```
for(i= 0;; i++){  
#pragma _stash1_top      MACRO_A(i, j);  
    if( j > INT_MAX)  
        break;  
}  
#pragma _stash1_end  
return( j );
```

FIG.12

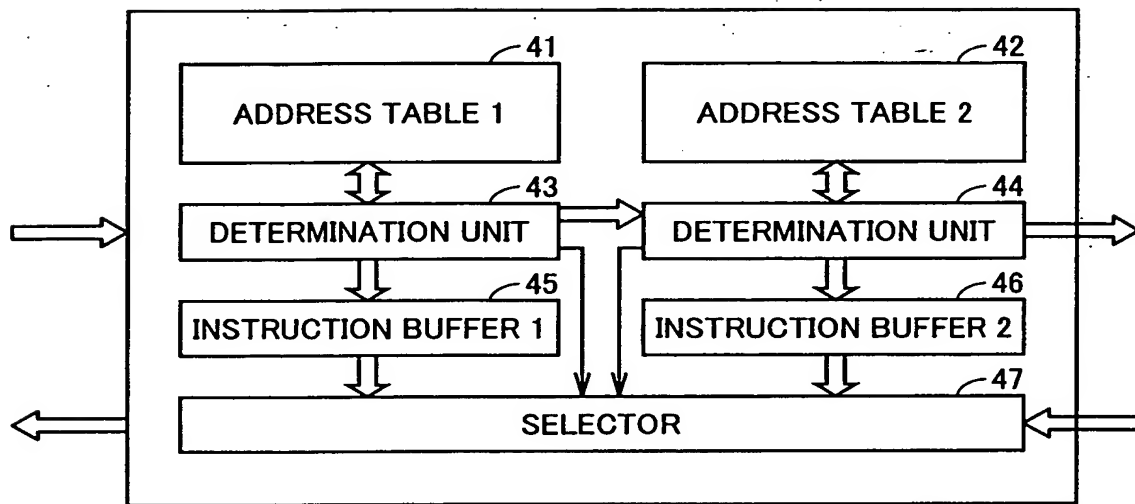


FIG.13

ADDRESS	
0x4000_0000	STASH_CNT
0x4000_0004	STASH_STS
	(USE PROHIBITED REGION)
0x4000_1000	ADDRESS TABLE 1
}	
0x4000_1FFF	
0x4000_2000	ADDRESS TABLE 2
}	
0x4000_2FFF	
0x4000_3000	INSTRUCTION BUFFER 1
}	
0x4000_3FFF	
0x4000_4000	INSTRUCTION BUFFER 2
}	
0x4000_4FFF	

FIG.14

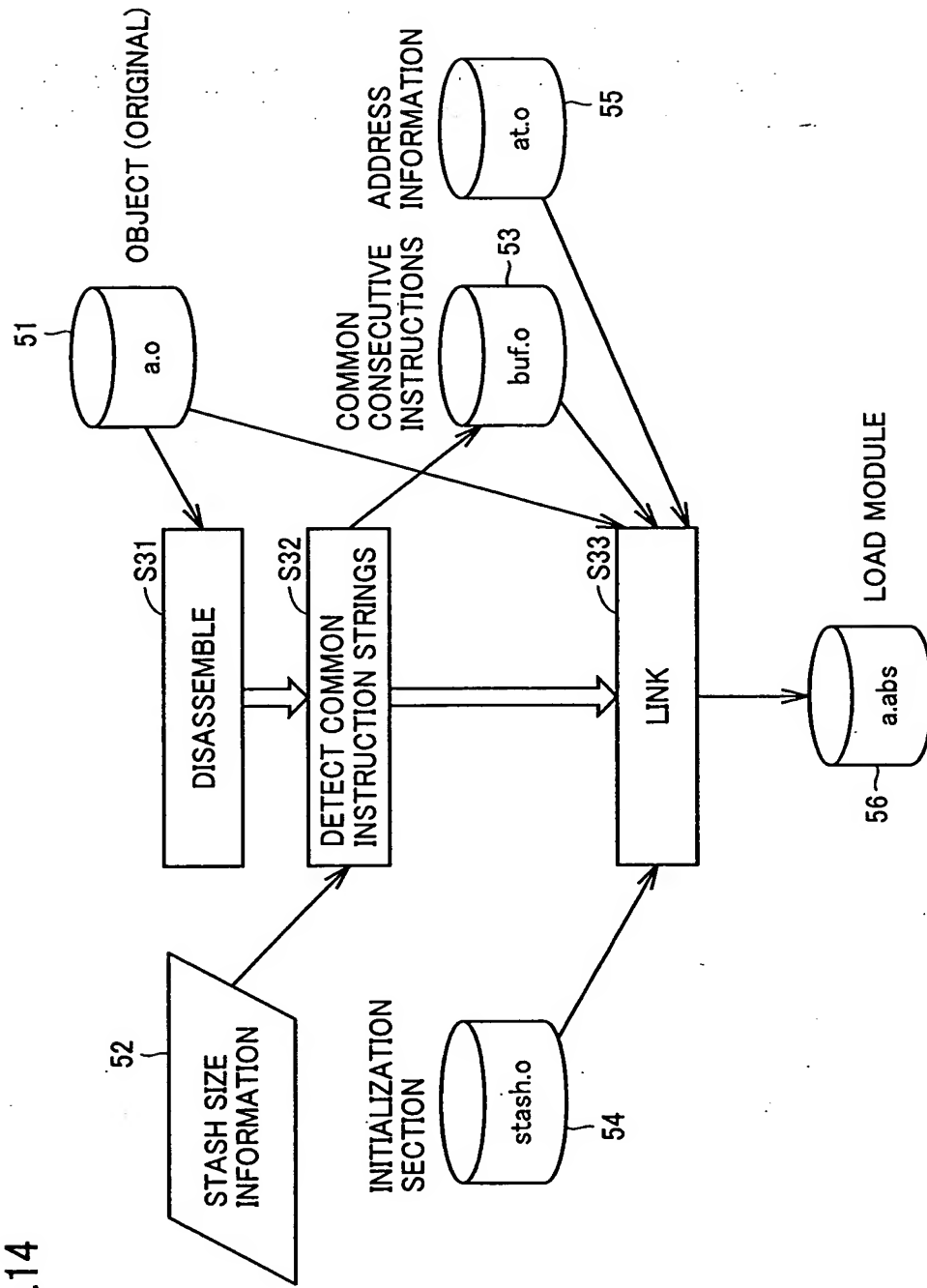


FIG.15

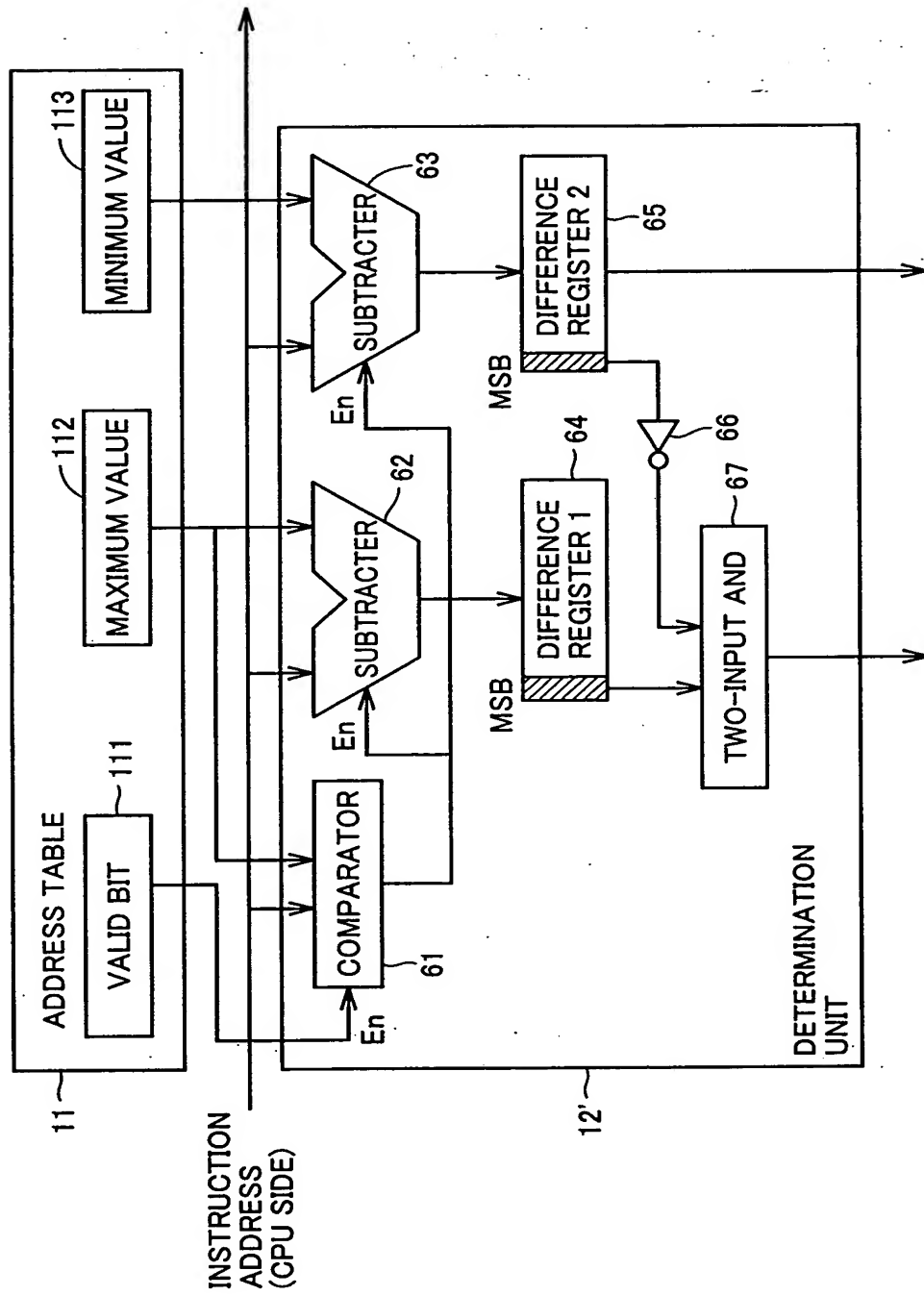


FIG.16

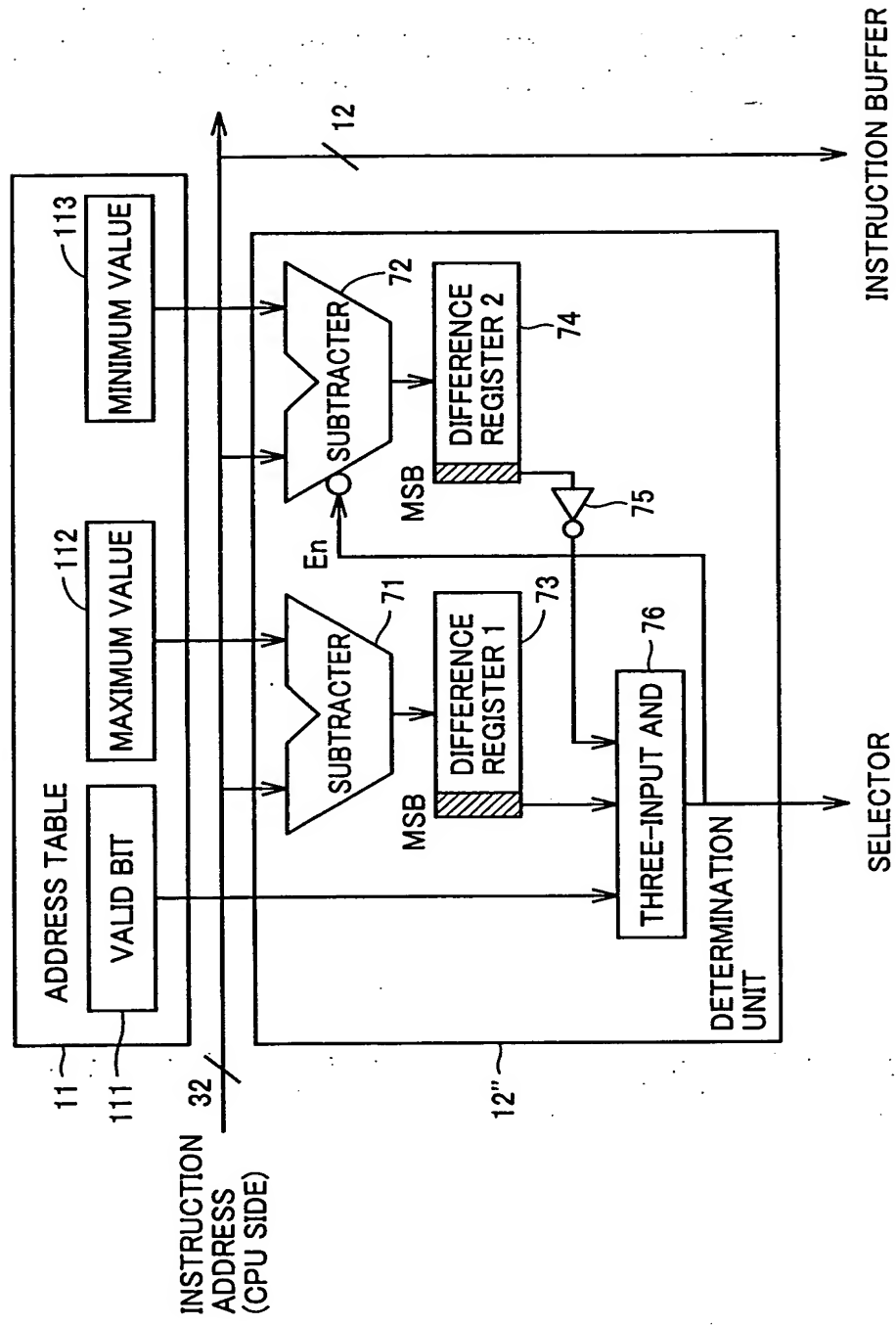


FIG.17

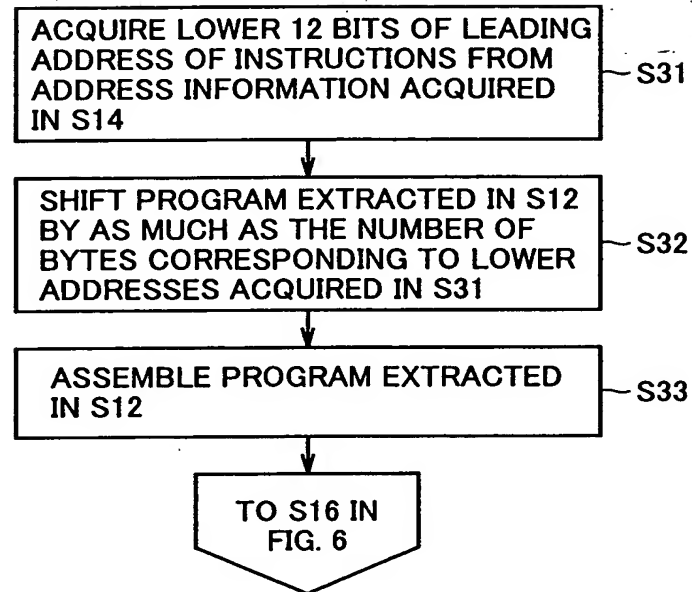


FIG.18

